## Notice of References Cited Application/Control No. 09/826,383 Applicant(s)/Patent Under Reexamination SHIGEYOSHI YOSHIDA Examiner Khiem D Nguyen Art Unit Page 1 of 1

## U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	Α	US-6,738,240	05-2004	Ahn et al.	361/38
	В	US-			
	С	US-			·
	D	US-			
,	E	US-			
	F	US-			
	G	US-			
	н	US-			
	1	US-			
	J	US-			
	К	US-			
	L	US-			
	М	US-			17

## **FOREIGN PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	0					
	Р					
	ø					
	R					
	s					
	Т					

## **NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Takahashi SoJi, Patent Abstracts Of Japan, "Wiring structure of semiconductor integrated circuit and forming method" (16.12.1997) Publication Number: 09-326587.
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	w	
	х	

\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).) Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.